

**ABSTRACT**

A liquid crystal display includes a plurality of gate lines ( $G_0 - G_n$ ), a plurality of data lines ( $D_1 - D_n$ ) formed in a direction crossing the gate lines, a plurality of pixel electrodes formed in a pixel area defined by the gate lines and the data lines, the pixel electrodes indicating pictures by a control of the corresponding gate lines, and a light volume adjusting layer formed on a lower layer of the pixel electrodes controlled by a second one of the gate lines ( $G_1$ ). A method for manufacturing a liquid crystal display includes the steps of: forming gate lines and a gate electrode on a substrate, forming a gate insulating film on the board including the gate electrode, forming a first active layer on the gate insulating film corresponding to an upper portion of the gate electrode and forming a second active layer on the gate insulating film corresponding to a portion where pixel electrodes are formed, forming source/drain electrodes on an upper portion of the first active layer, and forming a passivation film on the whole surface of the active layer including the source/drain electrodes.